

FIG. 1

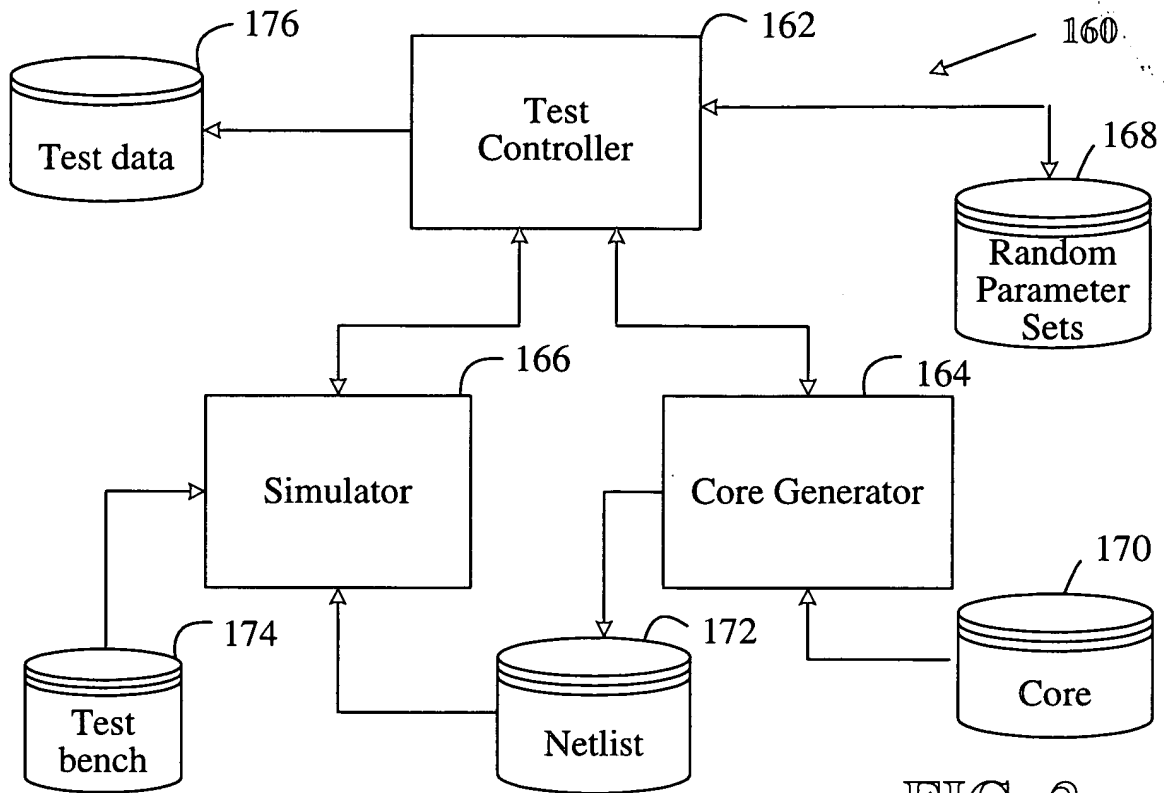


FIG. 2

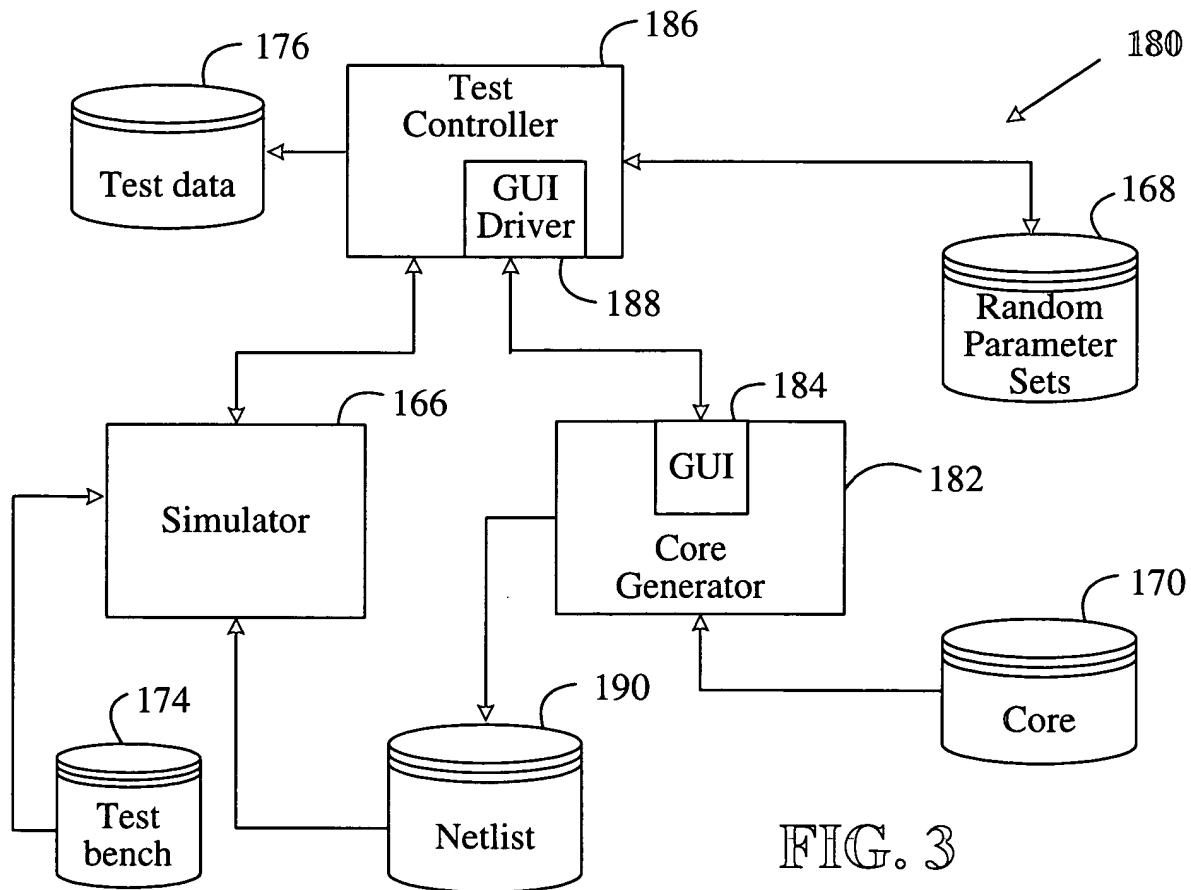


FIG. 3

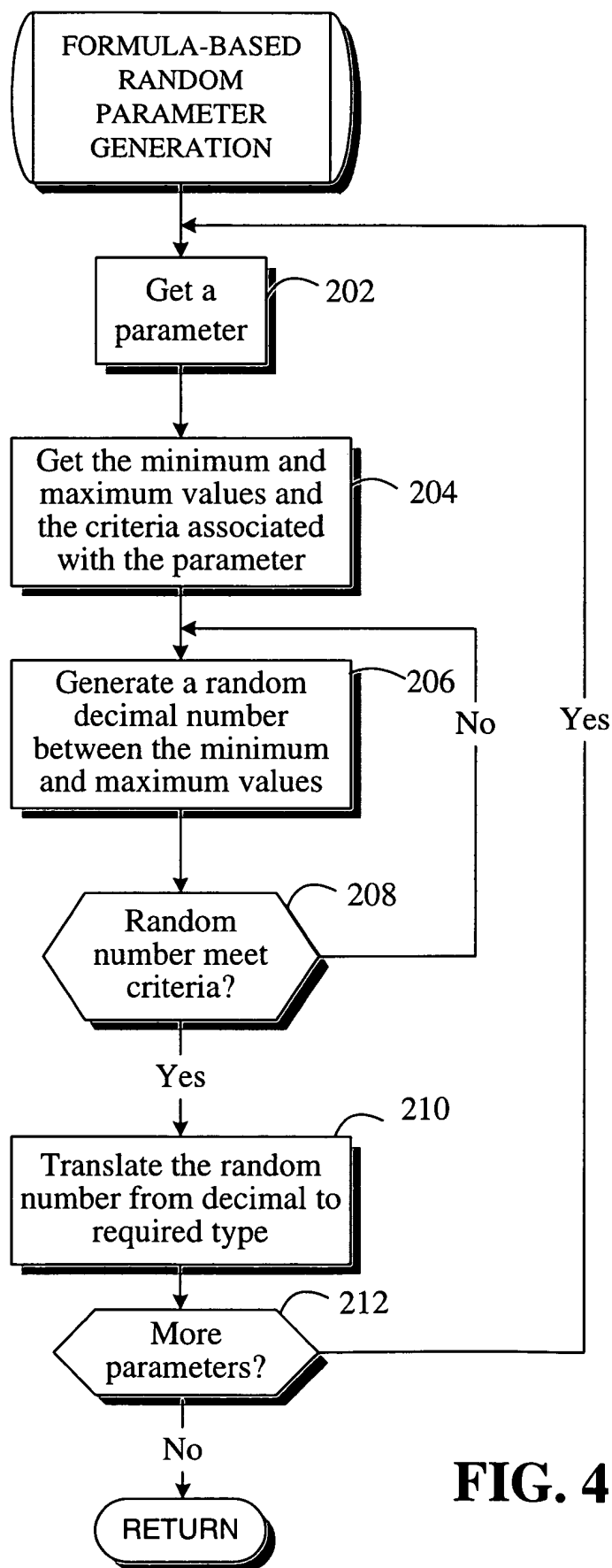


FIG. 4

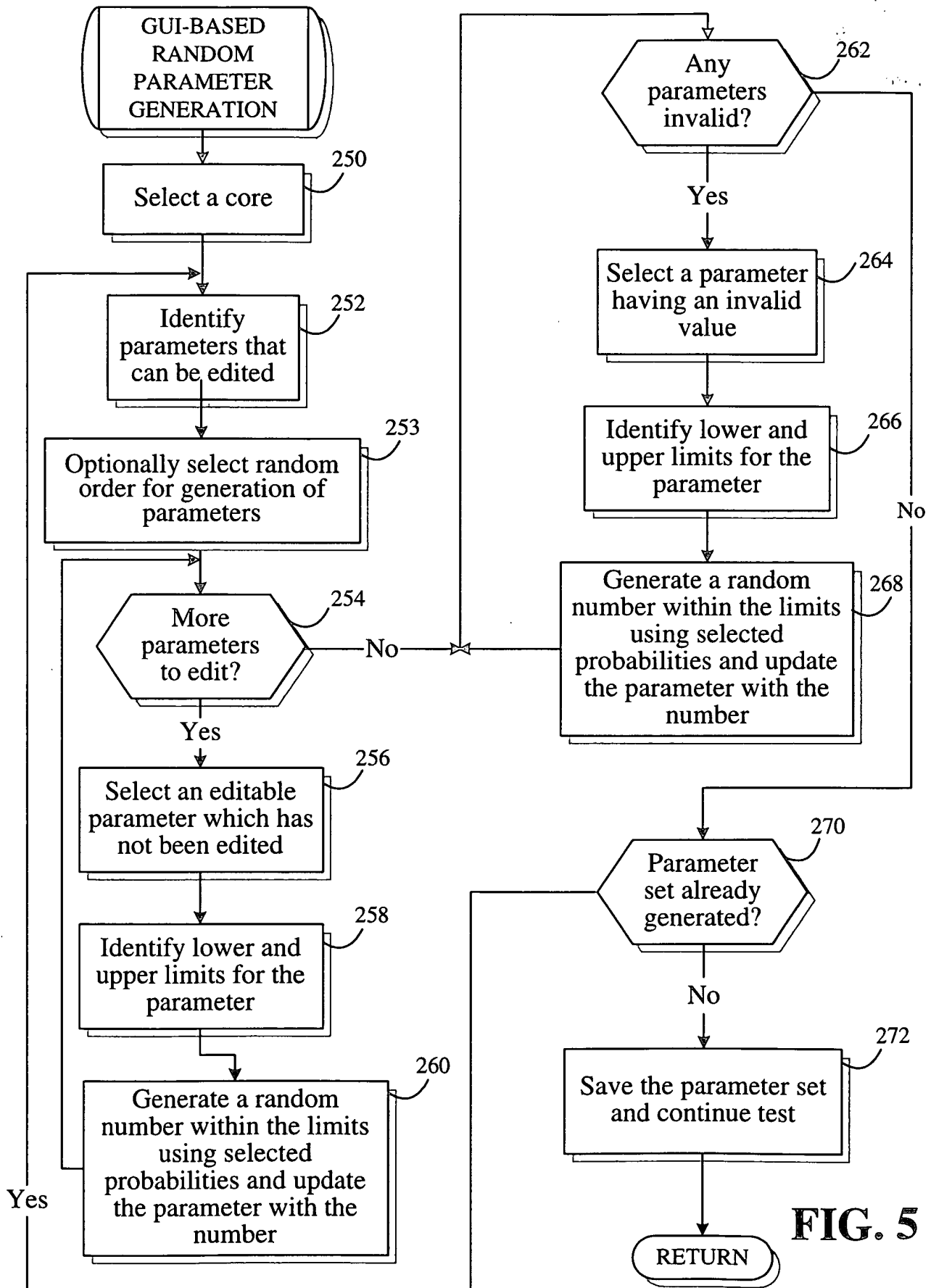


FIG. 5

APPROVED	O.G. FIG.	
BY.	CLASS	SUBCLASS
DRAFTSMAN		

Synchronous FIFO

Parameters

Core Overview

Contact

dr

we

re

reset

clk

full

empty

burst_ce

burst_updn

q

Synchronous FIFO

Component Name: fifo

Port Width: -5 (4 - 80)

Depth: 48

Single Port R

Dual Port RA

[allows sim

s read/write]

Generate

Cancel

Data Sheet...

logiCORE

FIG. 6A

APPROVED	O.G. FIG.	
BY	CLASS	SUBCLASS
DRAFTSMAN		

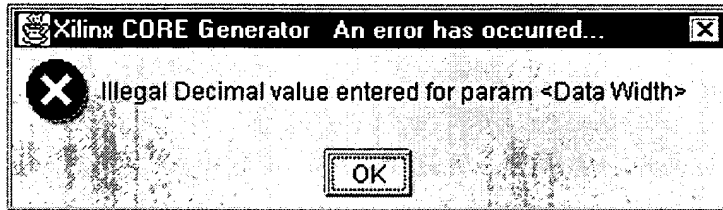


FIG. 6B

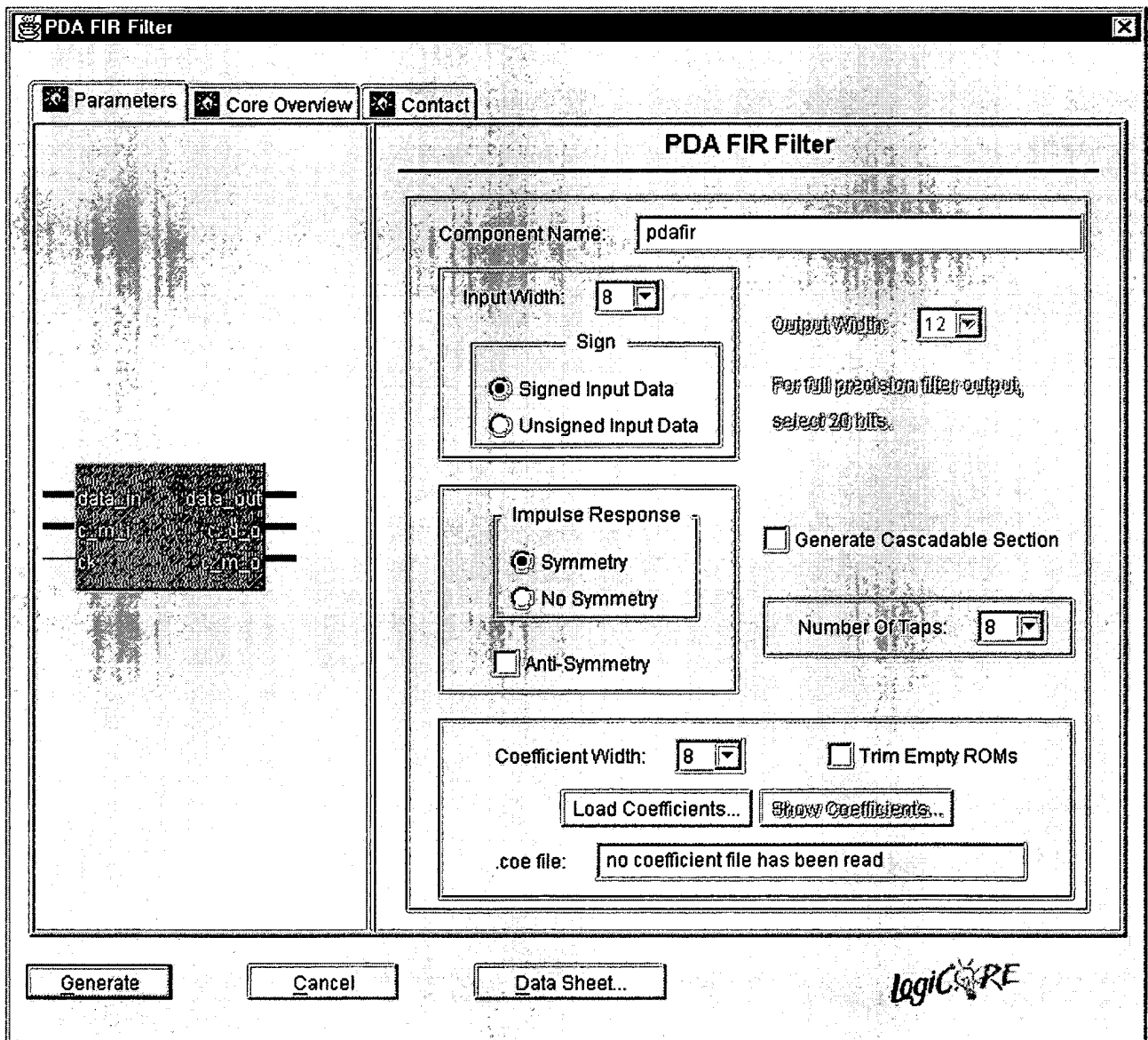


FIG. 6C

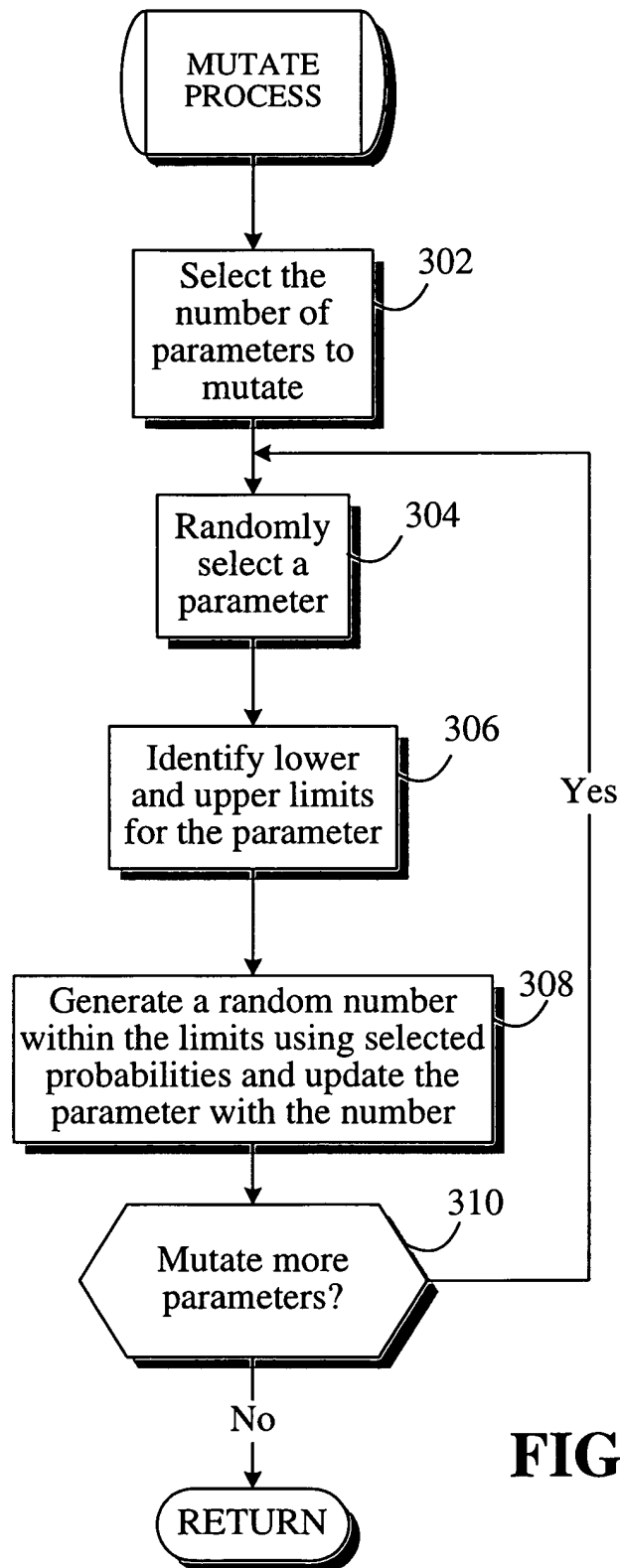


FIG. 7